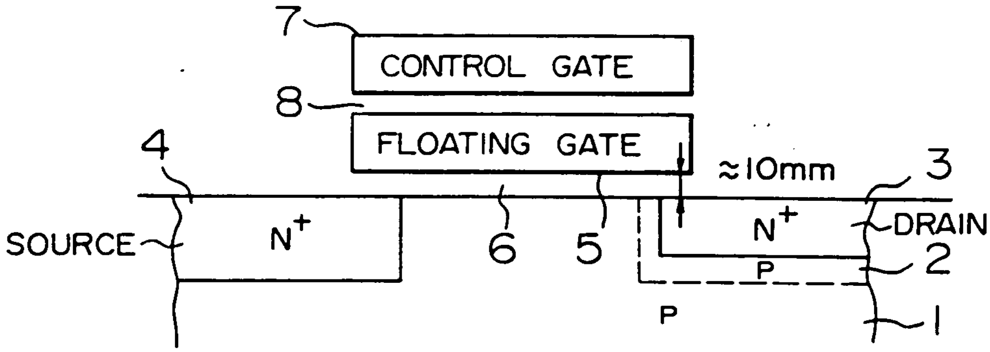
F I G. 1A



F I G. 1B

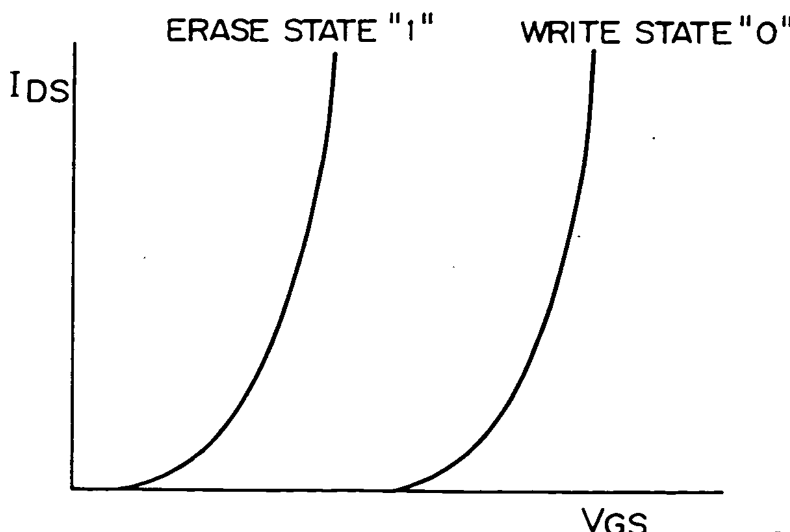


FIG. 2

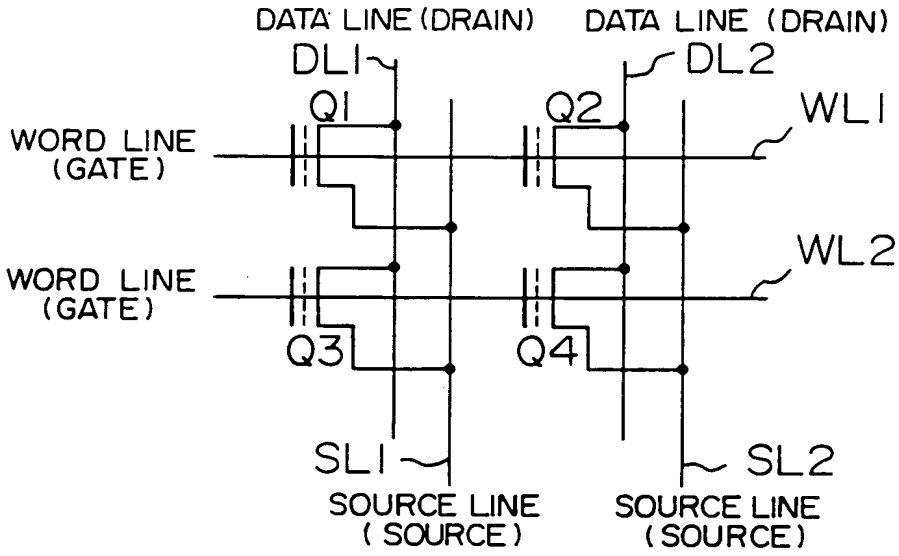


FIG. 9

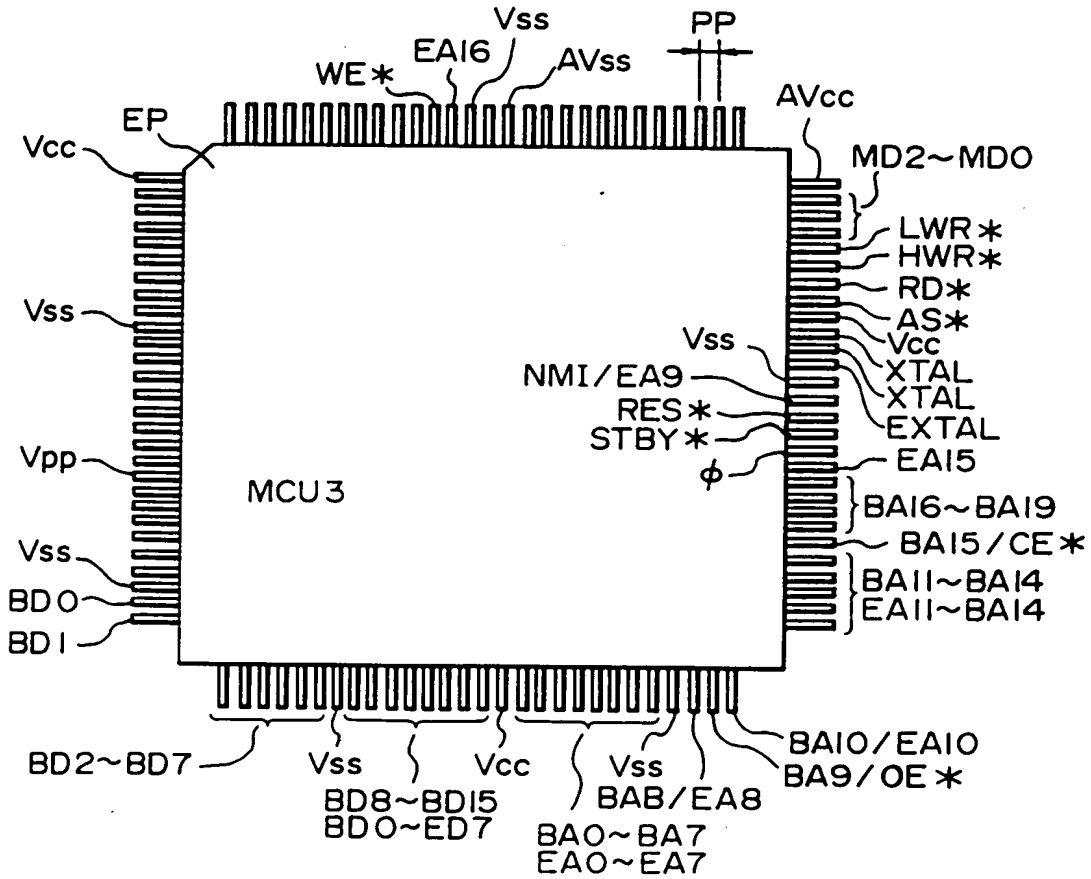


FIG. 3

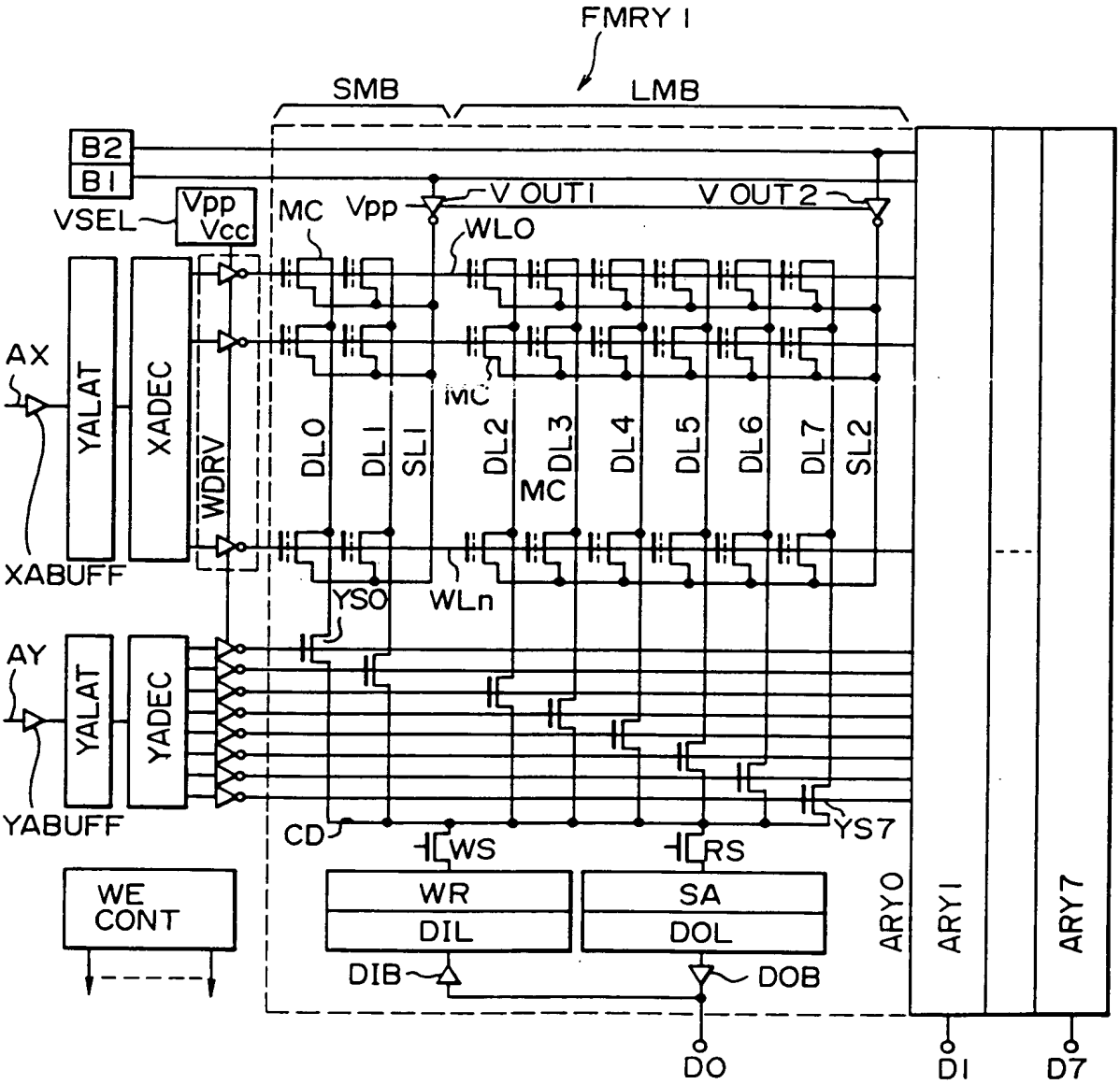


FIG. 4

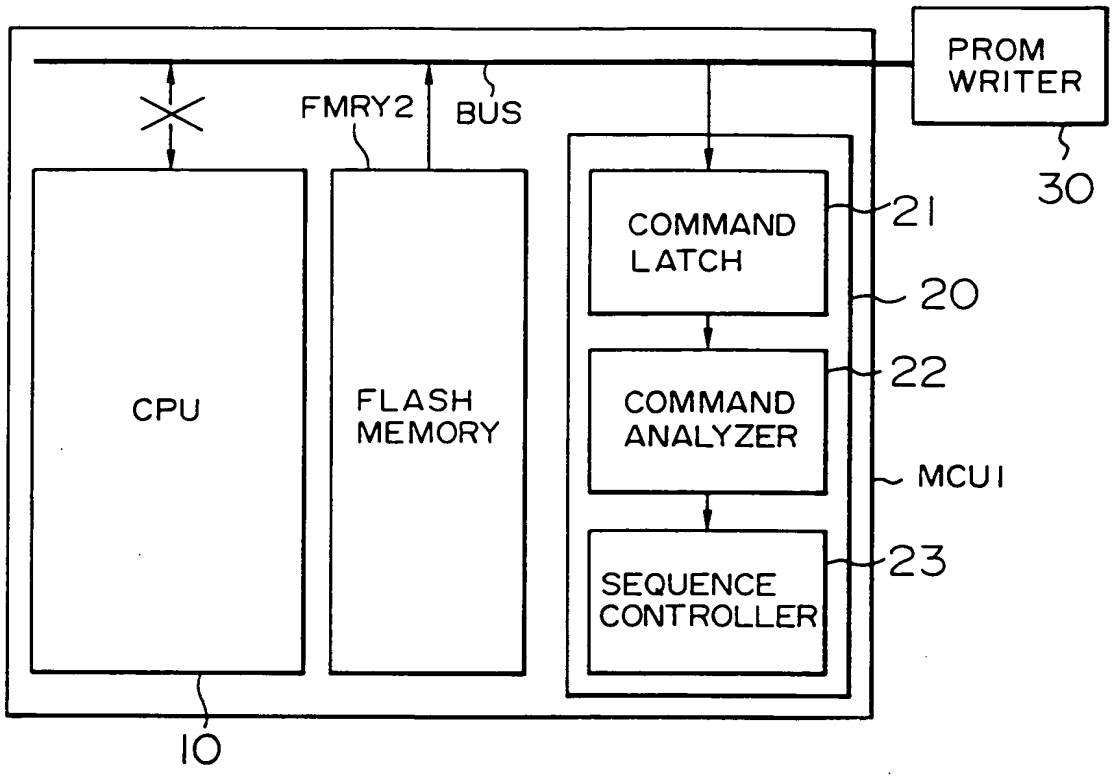


FIG. 5

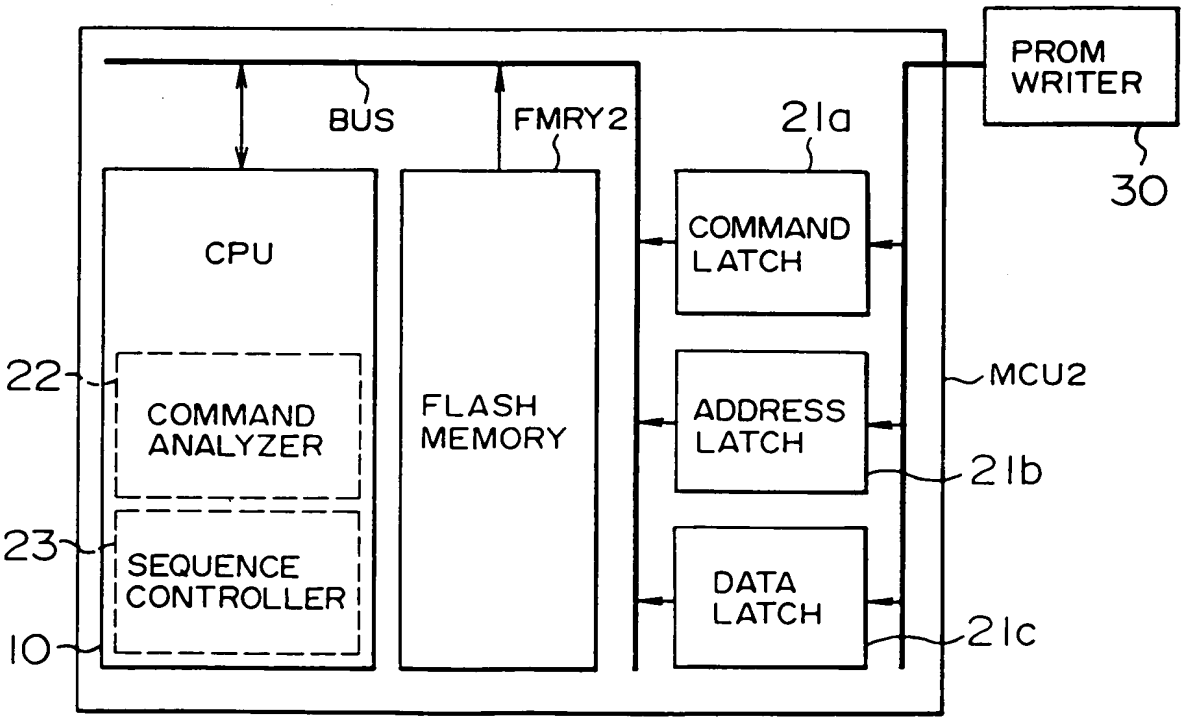


FIG. 6A

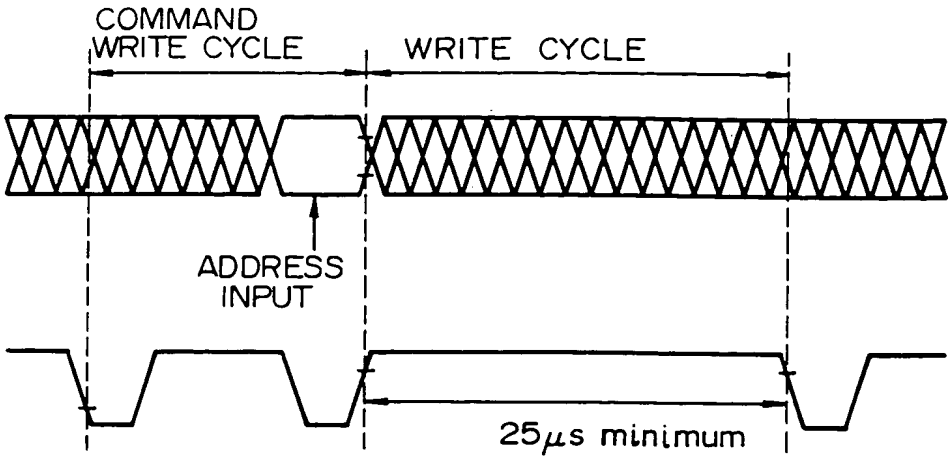


FIG. 6B

FIG. 6C

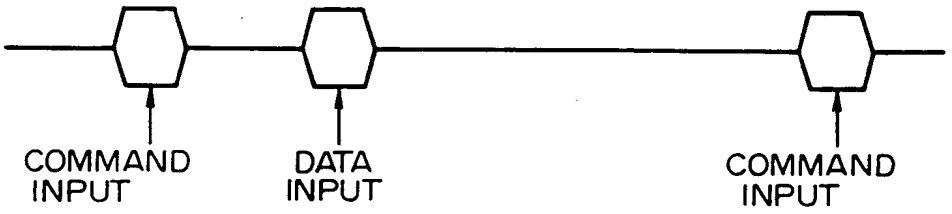


FIG. 7

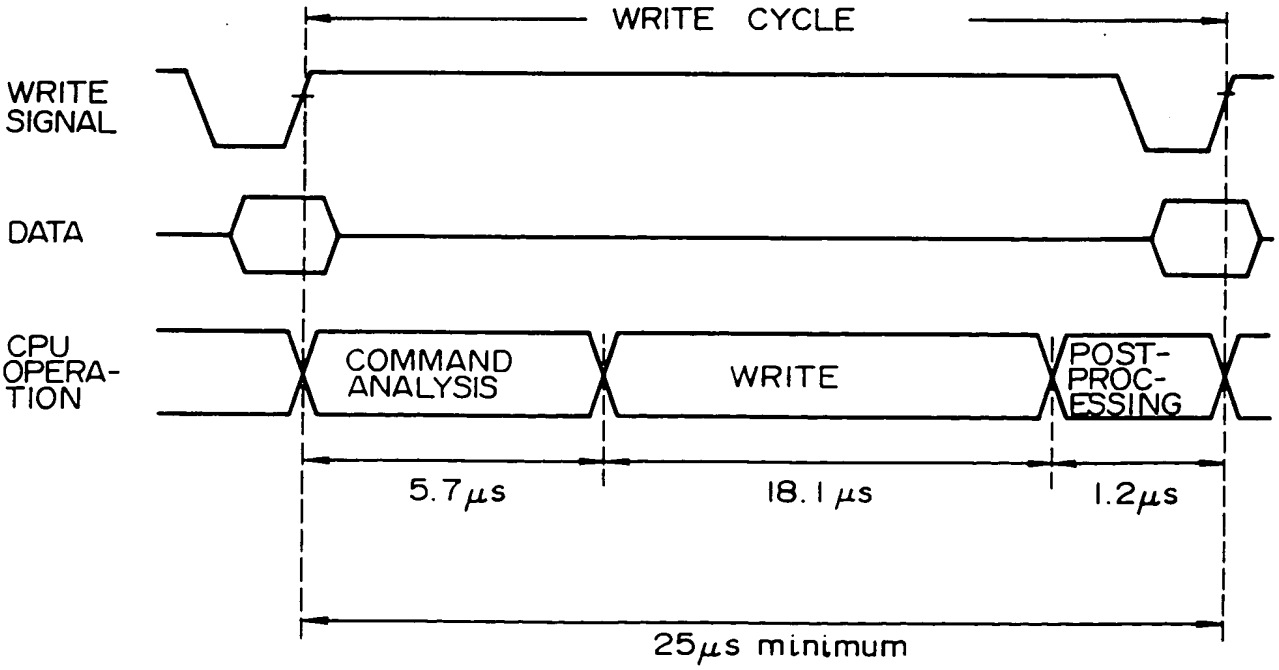


FIG. 8

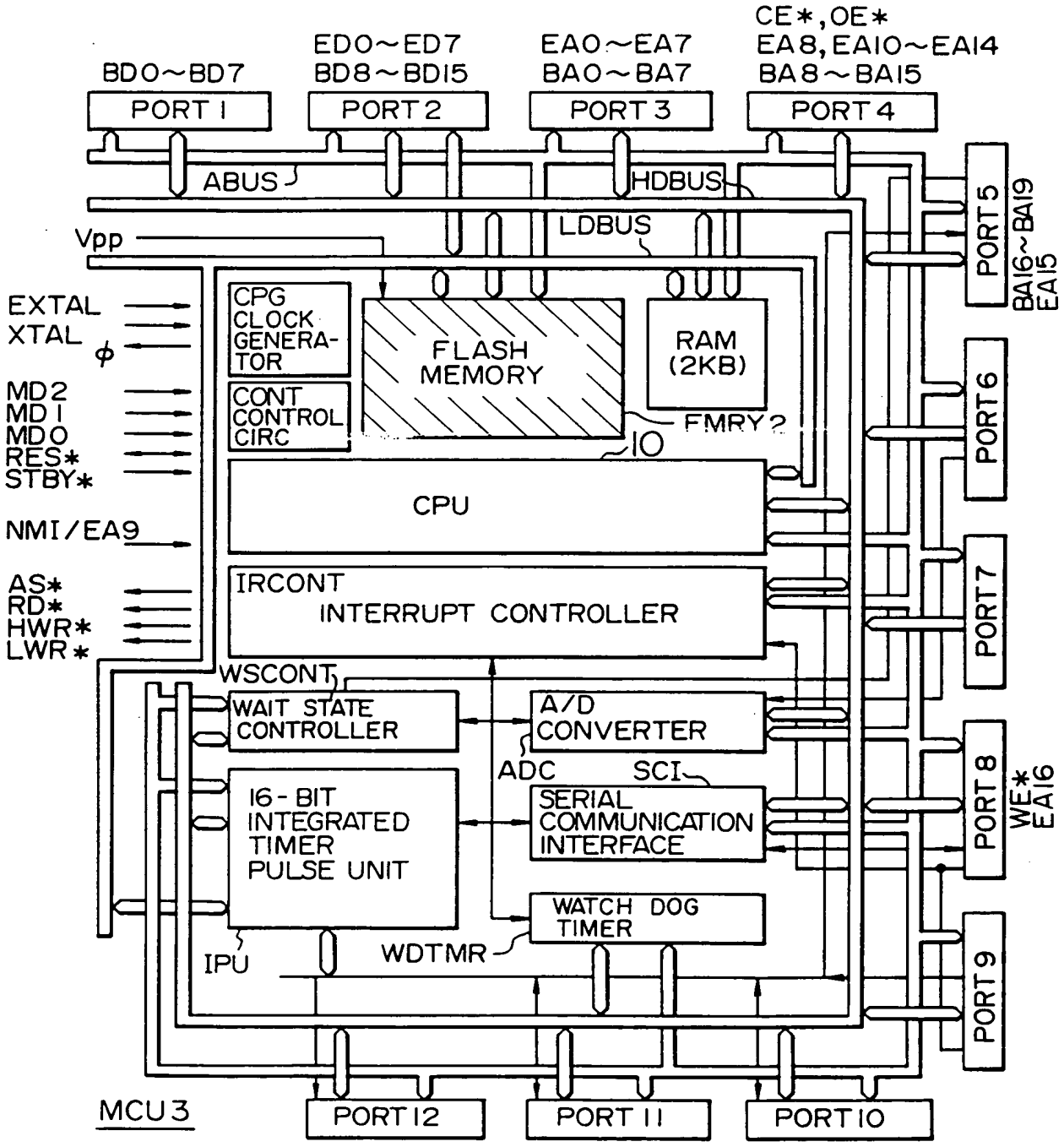


FIG. 10

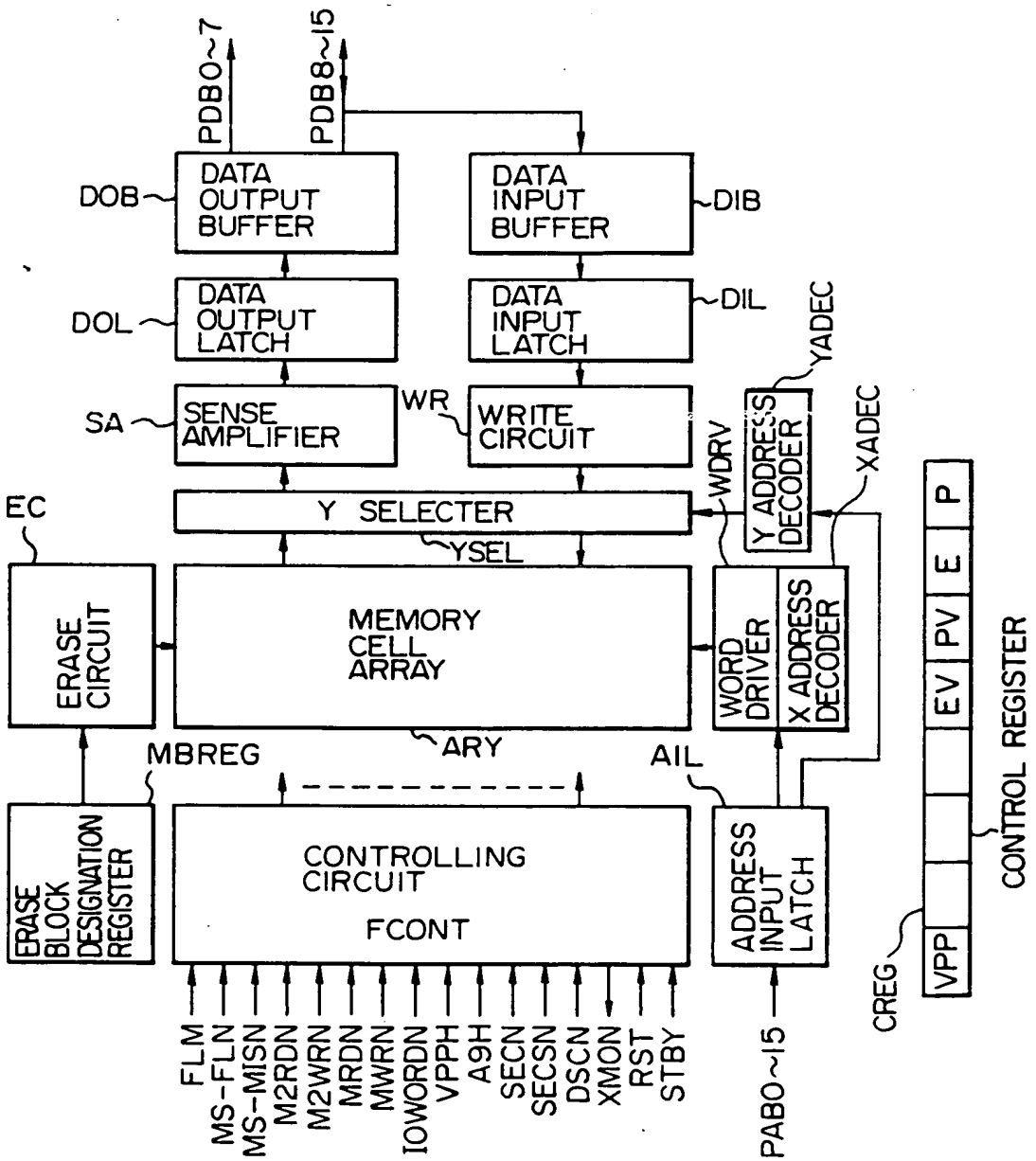


FIG. 11

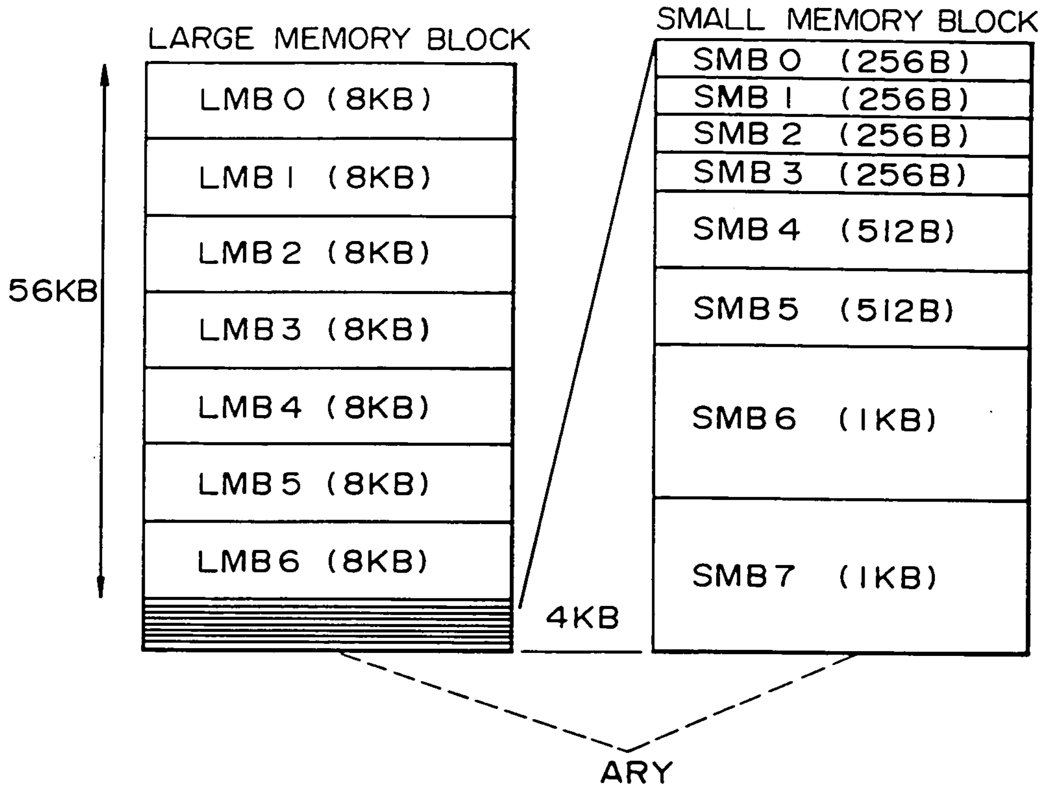
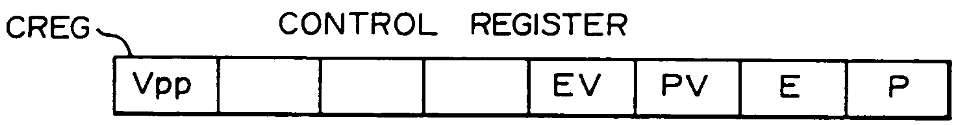


FIG. 12A



Vpp Vpp APPLICATION FLAG
EV ERASE VERIFY FLAG
PV PROGRAM VERIFY FLAG
E ERASE FLAG
P PROGRAM FLAG

FIG. 12B ERASE BLOCK DESIGNATION REGISTER FOR LARGE MEMORY BLOCKS

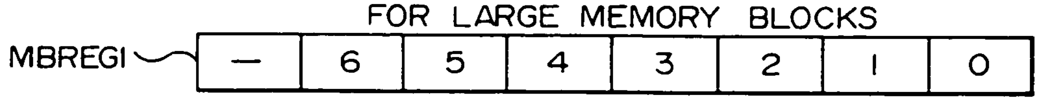
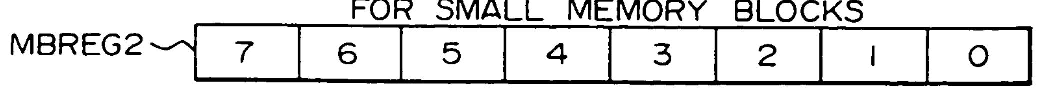


FIG. 12C ERASE BLOCK DESIGNATION REGISTER FOR SMALL MEMORY BLOCKS



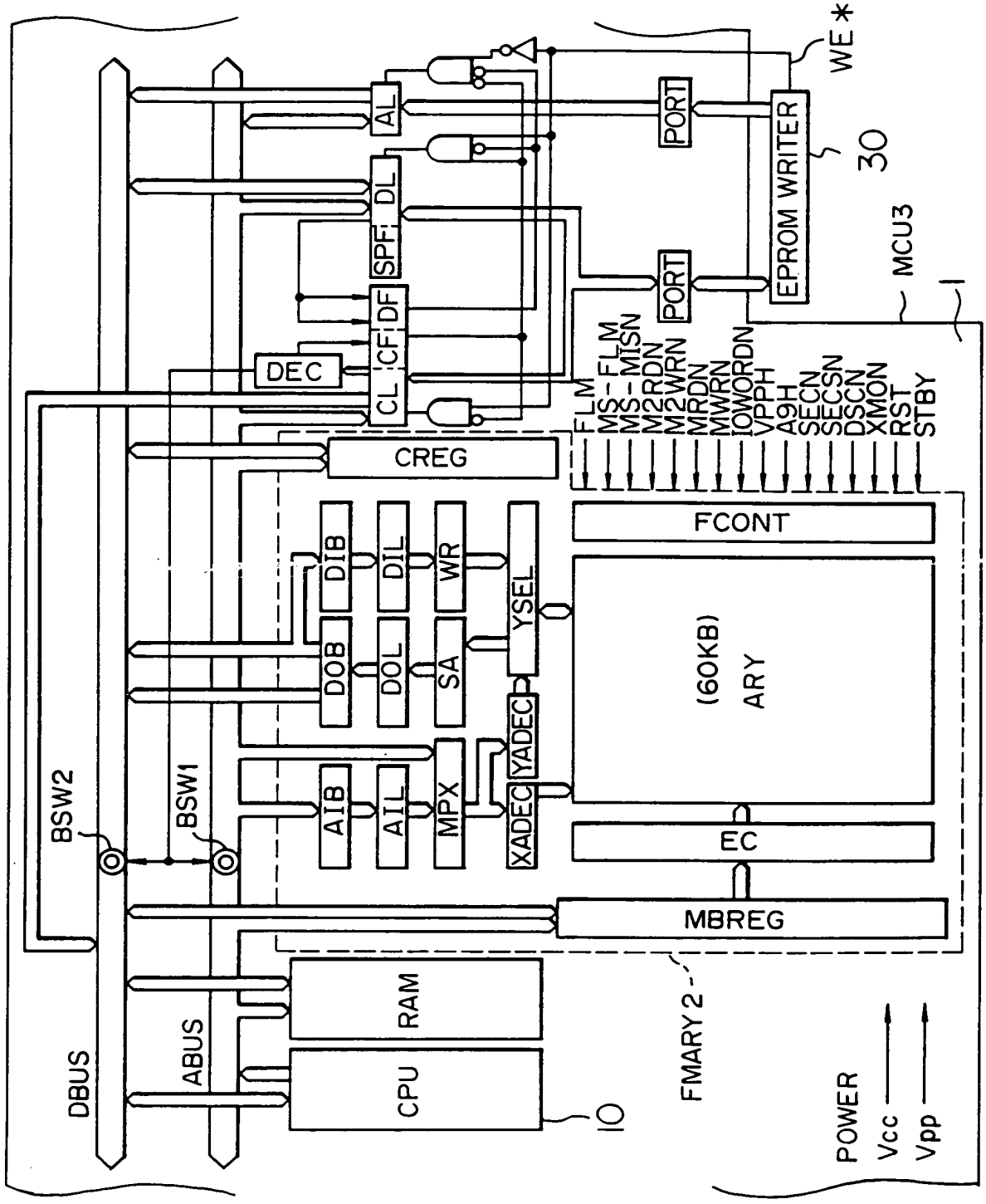


FIG. 13

FIG. 14

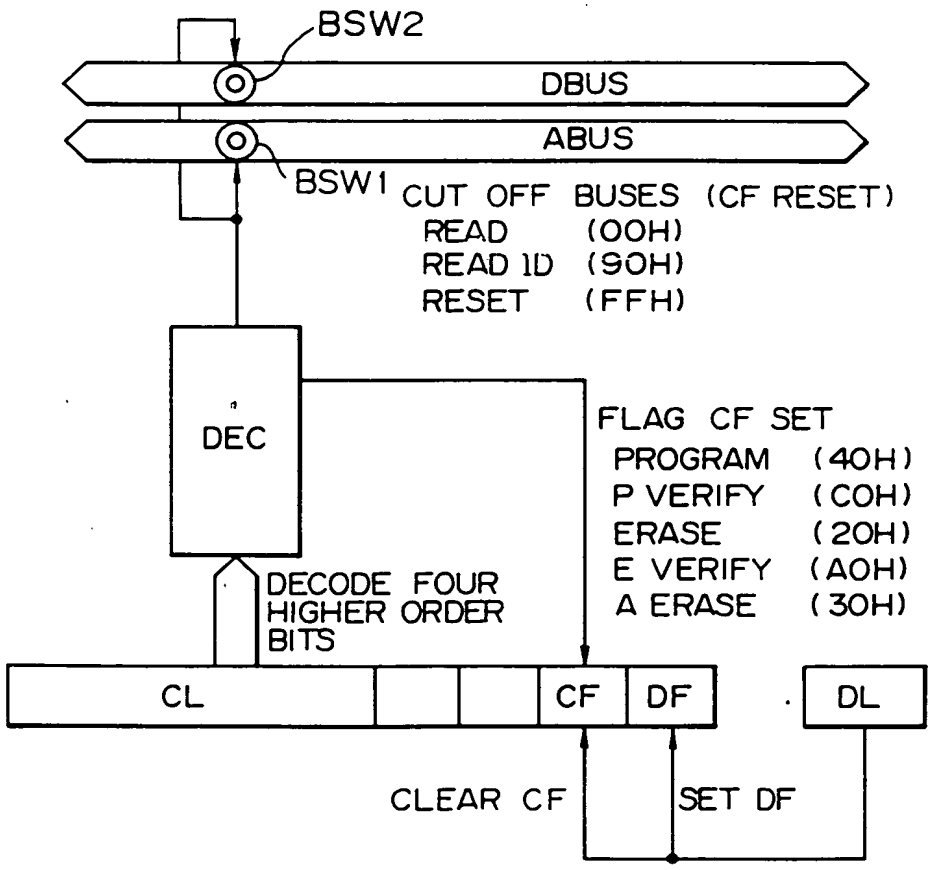


FIG. 15A

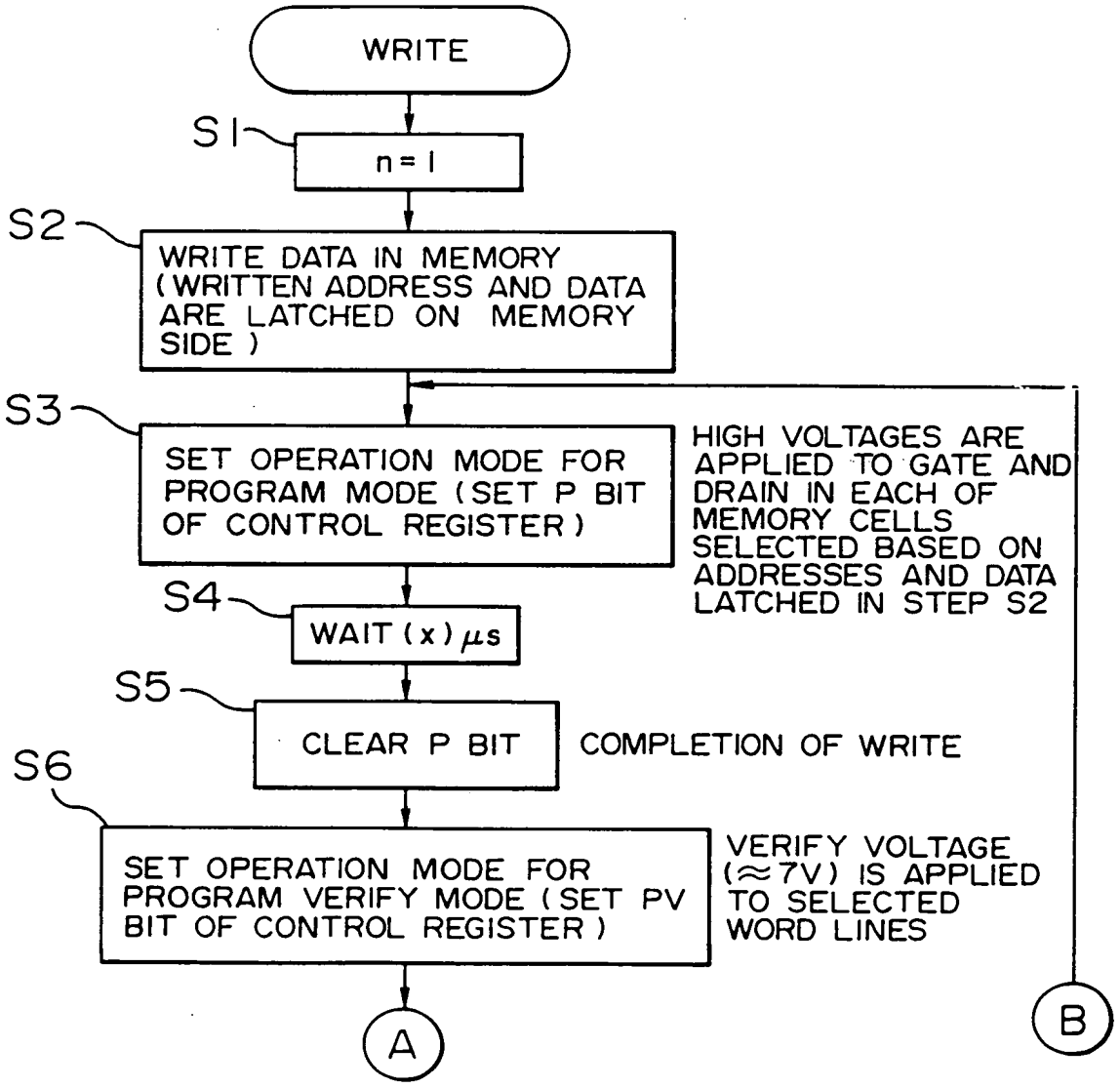


FIG. 15B

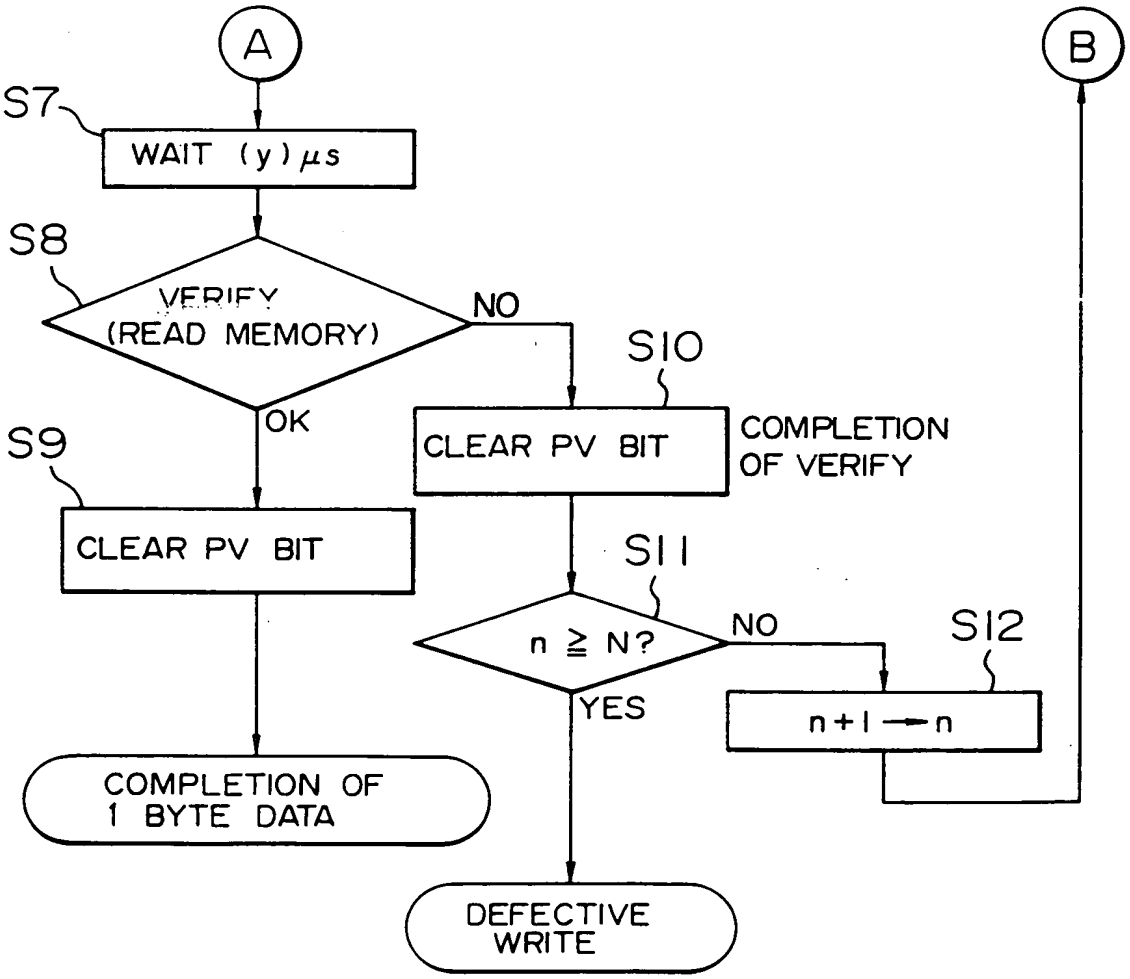
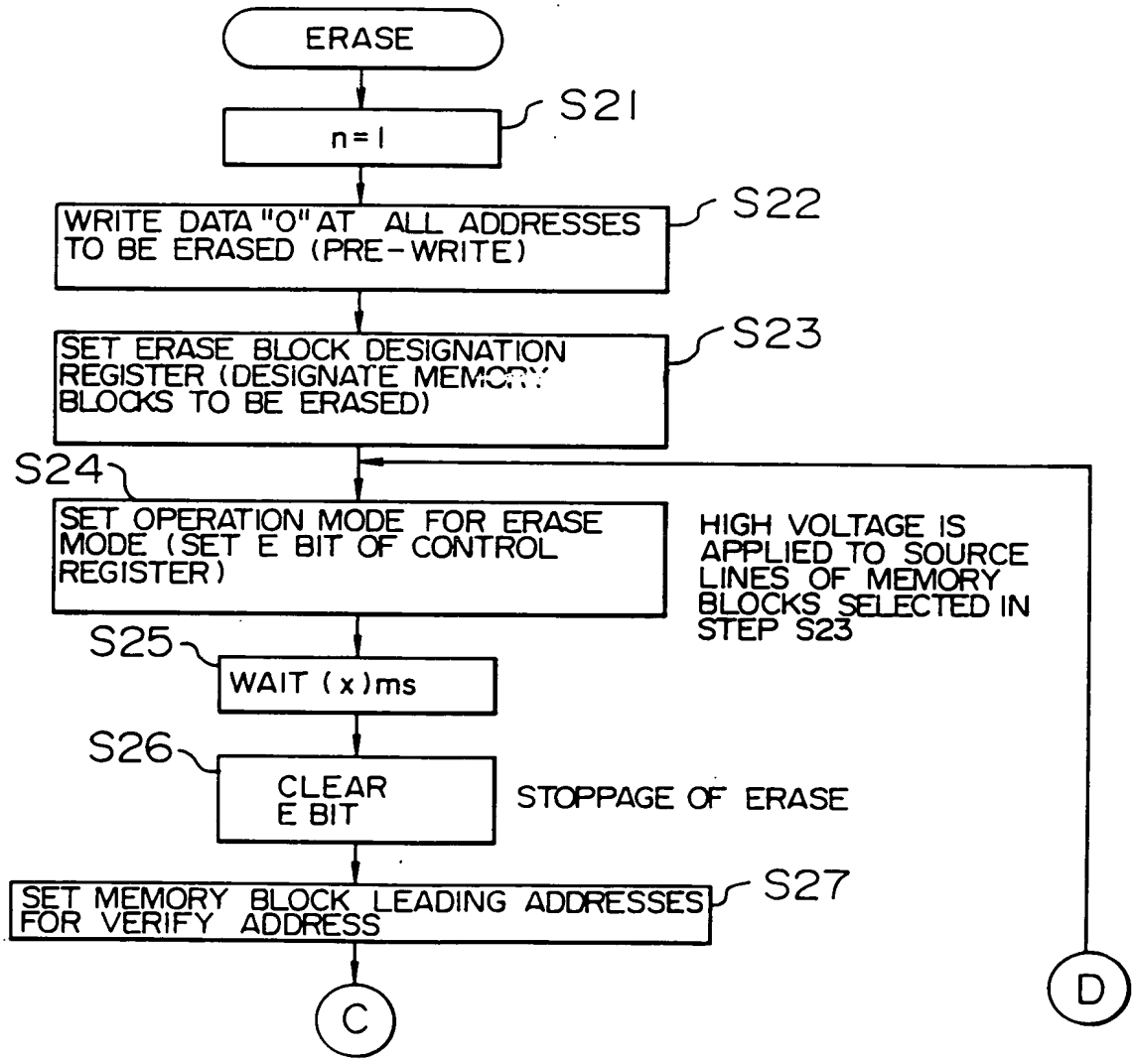


FIG. 16A



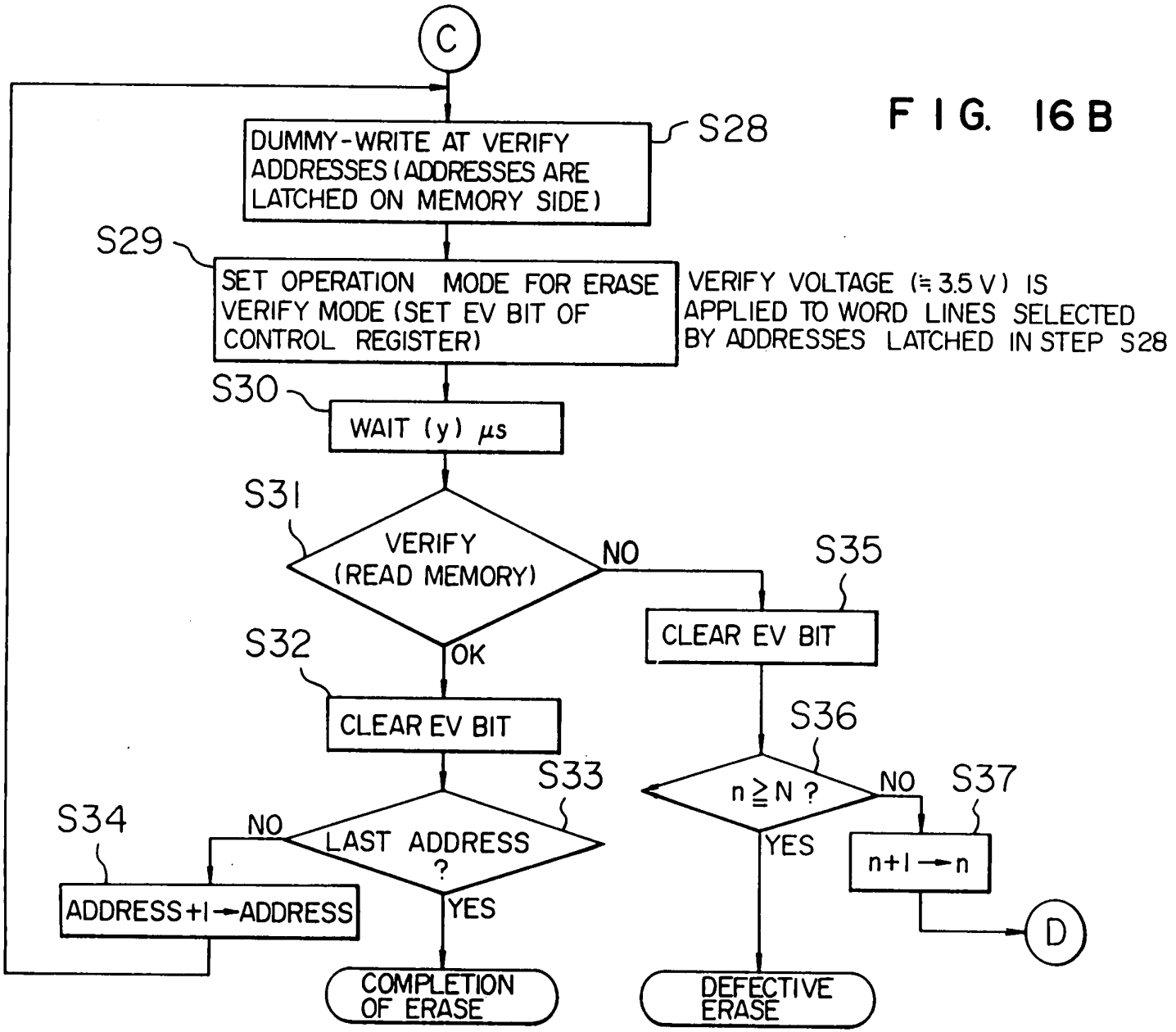


FIG. 16B

FIG. 17

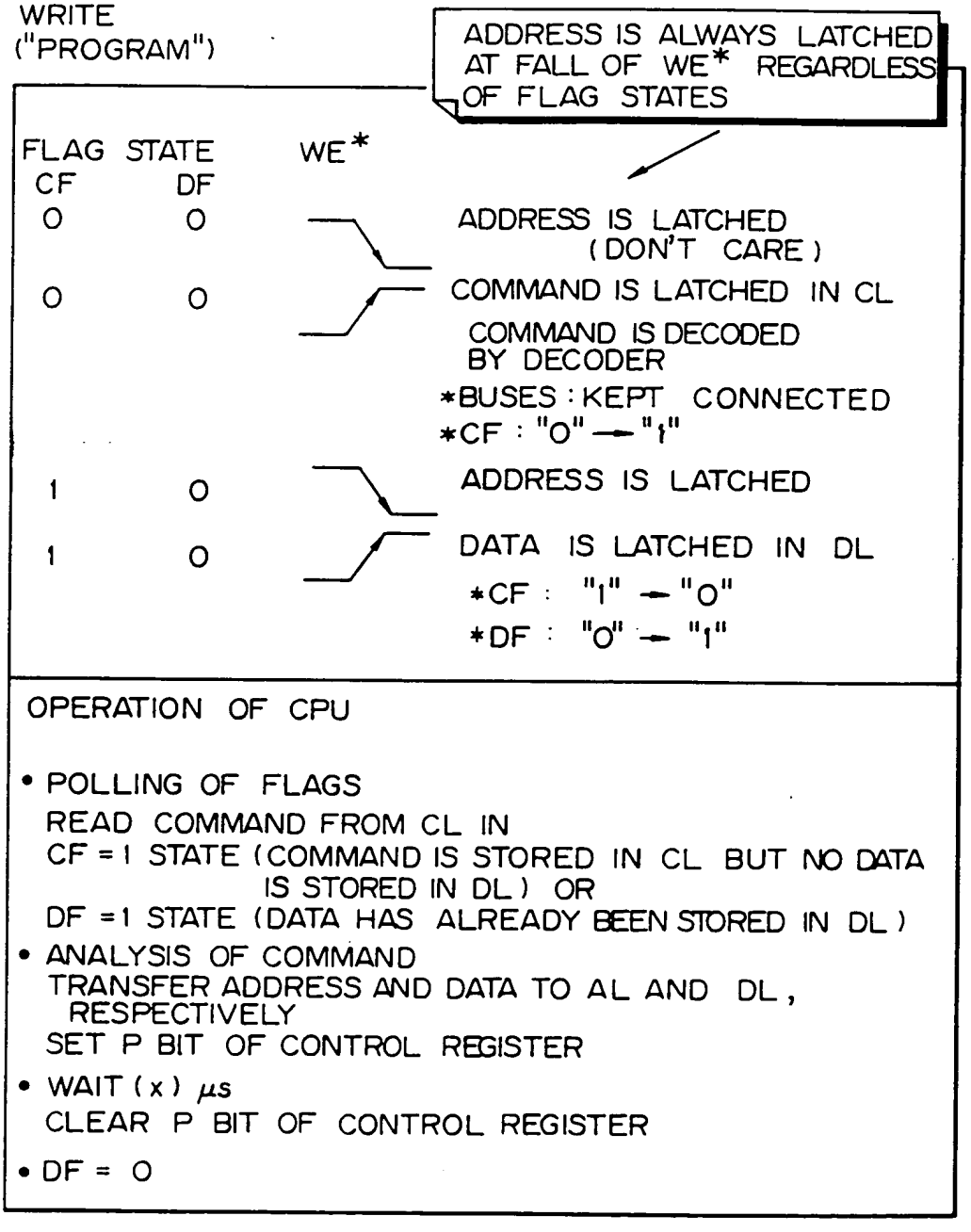


FIG. 18

WRITE VERIFY ("P VERIFY")

FLAG STATE		WE*	
C F	D F		
0	0		ADDRESS IS LATCHED (DON'T CARE)
0	0		COMMAND IS LATCHED IN CL COMMAND IS DECODED BY DECODER

*BUSES : KEPT CONNECTED
*CF : "0" → "1"

OPERATION OF CPU

- POLLING OF FLAGS

  READ COMMAND FROM CL IN

  C F = 1 STATE (COMMAND IS STORED IN CL BUT NO DATA IS STORED IN DL) OR

  D F = 1 STATE (DATA HAS BEEN STORED IN DL)
- ANALYSIS OF COMMAND

  PV BIT OF CONTROL REGISTER IS SET

  READ DATA AT ADDRESS FOR WRITING BY USE OF THE ADDRESS FOR WRITING (STORED BY CPU)

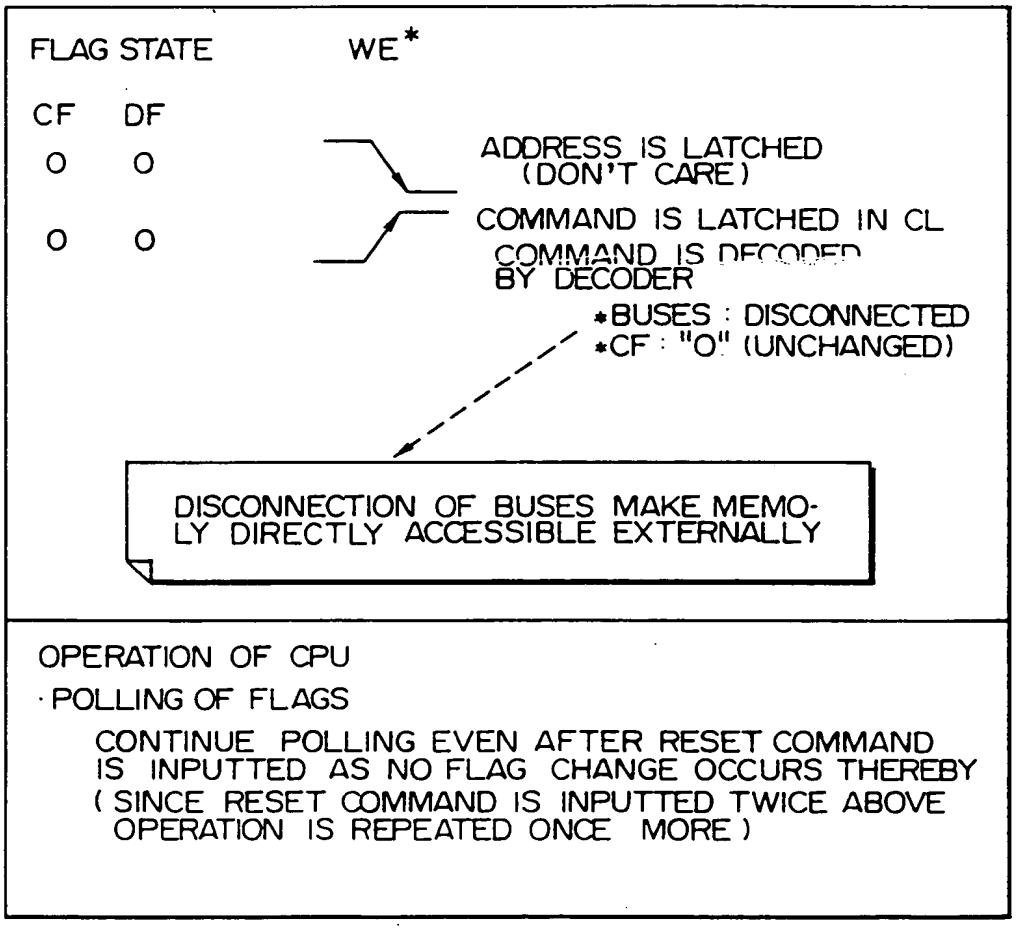
  CLEAR PV BIT OF CONTROL REGISTER
- C F = "1" → "0"

APPROVED BY DRAFTSMAN	O.G. FIG.	
	CLASS	SUBCLASS

FIG. 19A

RESET ("RESET")

(1) WHEN RESET COMMAND IS FIRST INPUTTED



OPERATION OF CPU

· POLLING OF FLAGS

CONTINUE POLLING EVEN AFTER RESET COMMAND IS INPUTTED AS NO FLAG CHANGE OCCURS THEREBY (SINCE RESET COMMAND IS INPUTTED TWICE ABOVE OPERATION IS REPEATED ONCE MORE)

APPROVED BY DRAFTSMAN	O.G. FIG.	
	CLASS	SUBCLASS

F I G. 19 B

RESET ("RESET")

(2) WHEN RESET COMMAND IS INPUTTED TO INTERRUPT ANOTHER COMMAND

FLAG STATE		WE*	
C F	D F		
I	O		ADDRESS IS LATCHED (DON' T CARE)
I	O		COMMAND IS LATCHED IN CL * C F : "I" → "O" * D F : "O" → "I" (UNDER CPU OPERATION) * D F : "I" → "O"
O	I		
O	O		ADDRESS IS LATCHED COMMAND IS LATCHED IN CL
O	O		COMMAND IS DECODED BY DECODER * BUSES : DISCONNECTED * C F : "O" (UNCHANGED)

OPERATION OF CPU

IF RESET COMMAND IS INPUTTED DURING WRITE MODE, "FFH" WILL BE WRITTEN AS DATA. HOWEVER, SINCE "FFH" REPRESENTS NO DATA TO BE WRITTEN, NO DATA WILL BE ACTUALLY WRITTEN.

IF RESET COMMAND IS INPUTTED DURING ERASE MODE, ERASE OPERATION WILL BE ENDED AFTER RECOGNITION OF THE 2ND COMMAND AS NOT BEING ERASE COMMAND.

FOR OPERATION AFTER 2ND COMMAND INPUT, ABOVE OPERATION IS FOLLOWED.

FIG. 20A

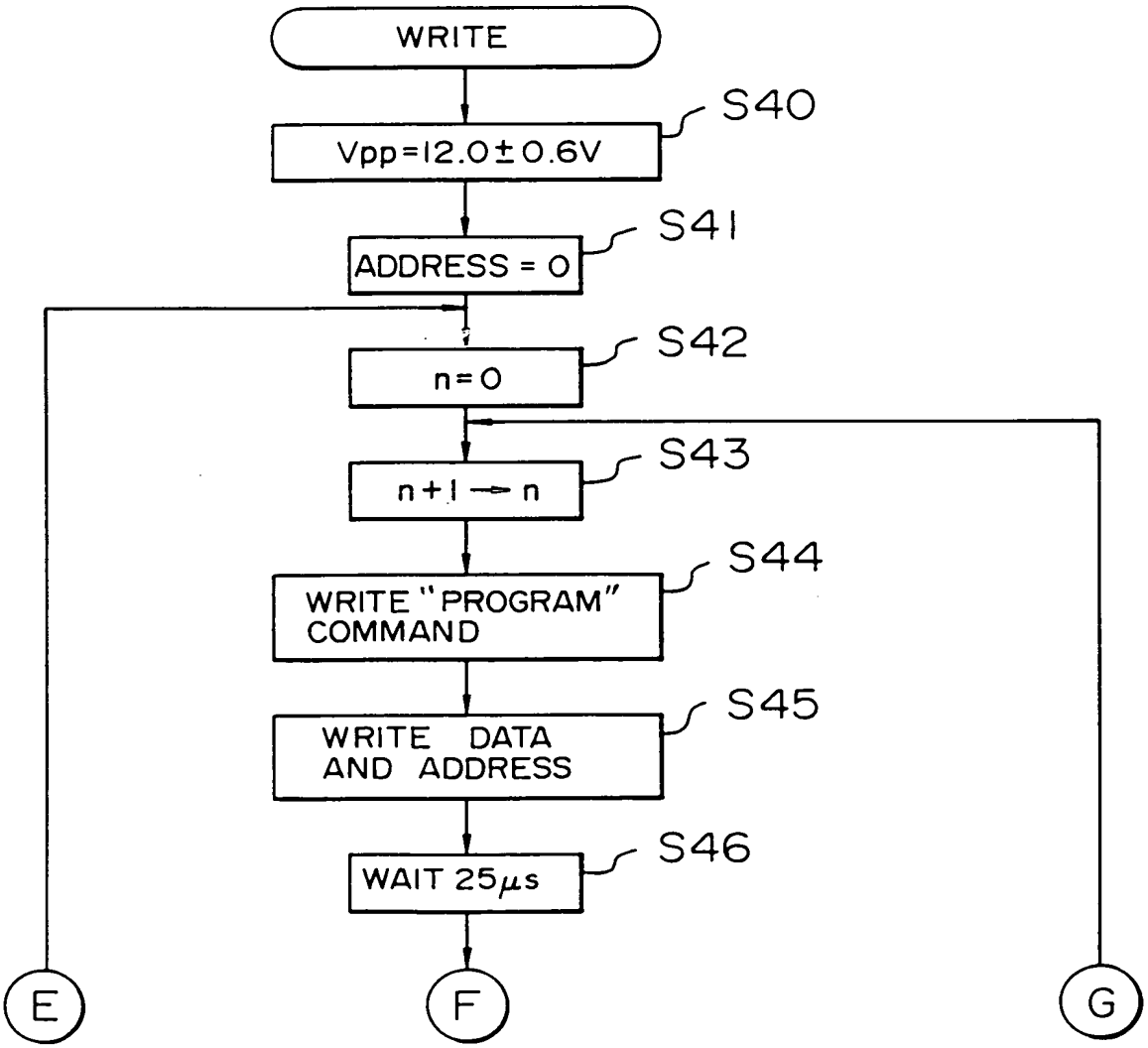


FIG. 20B

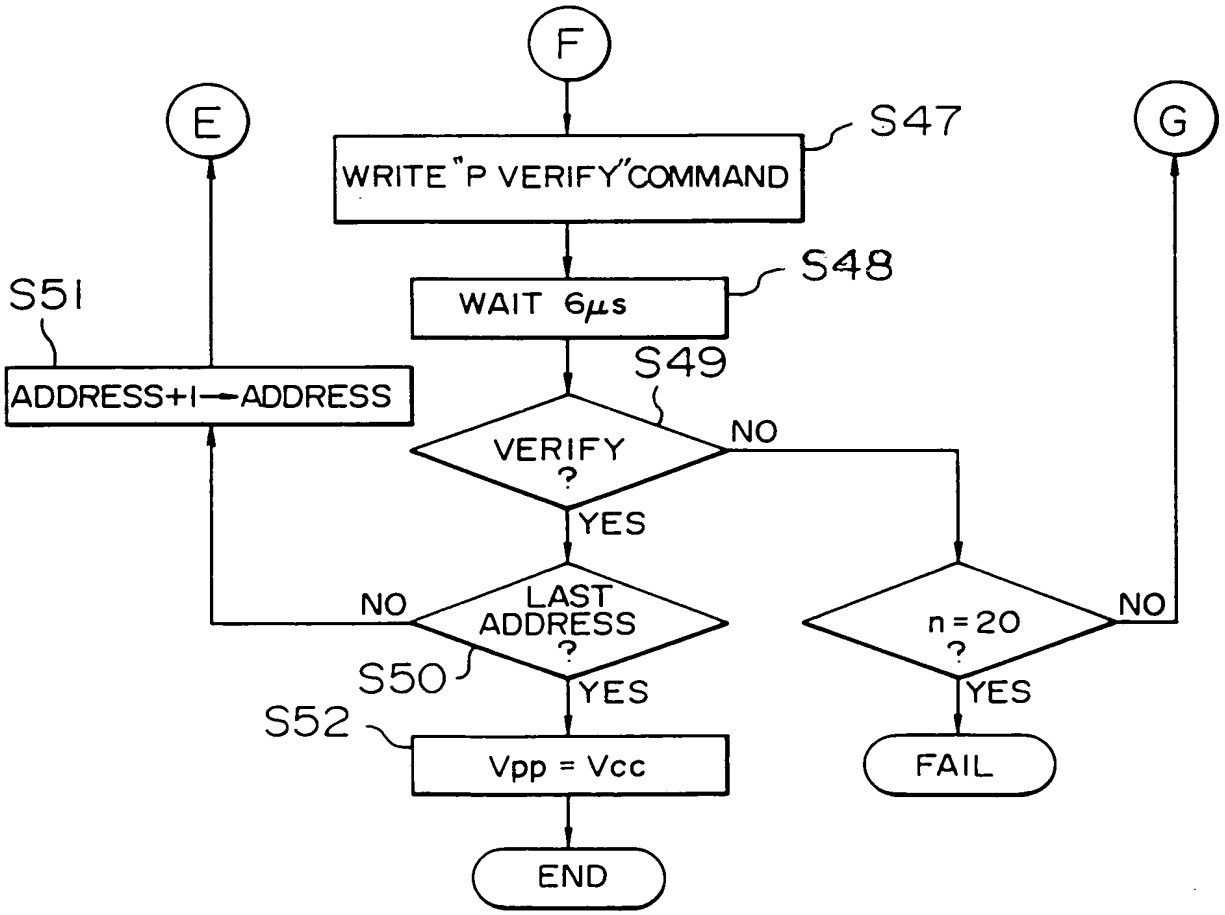


FIG. 21A

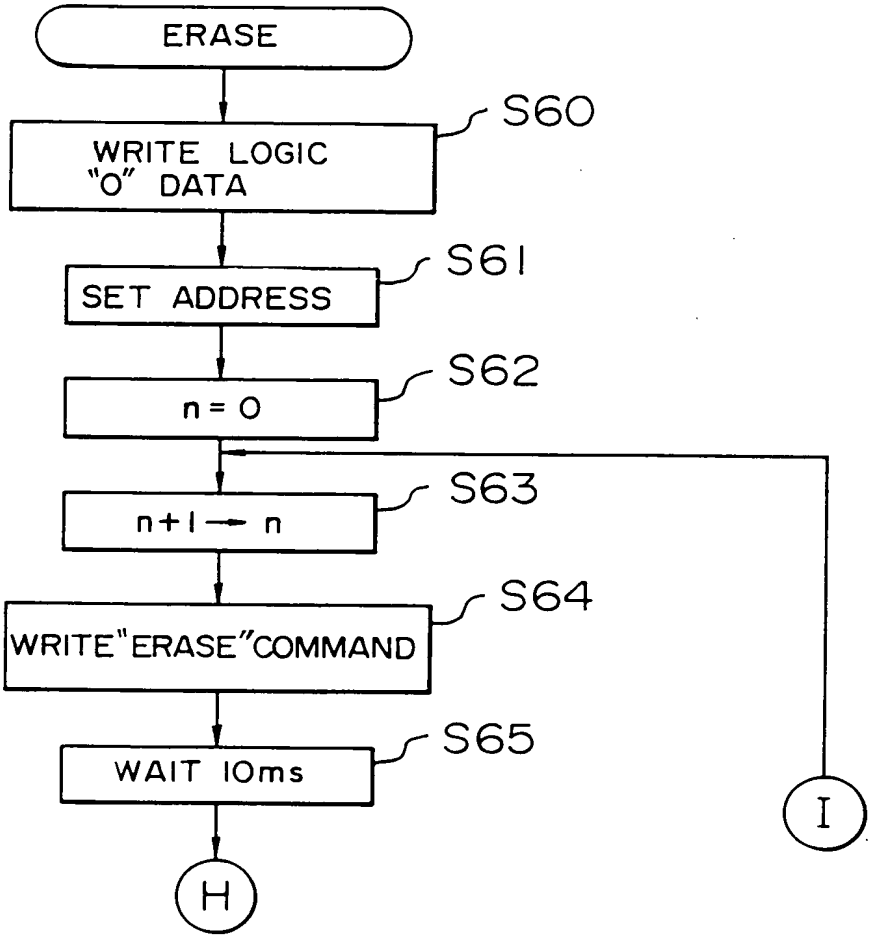


FIG. 21B

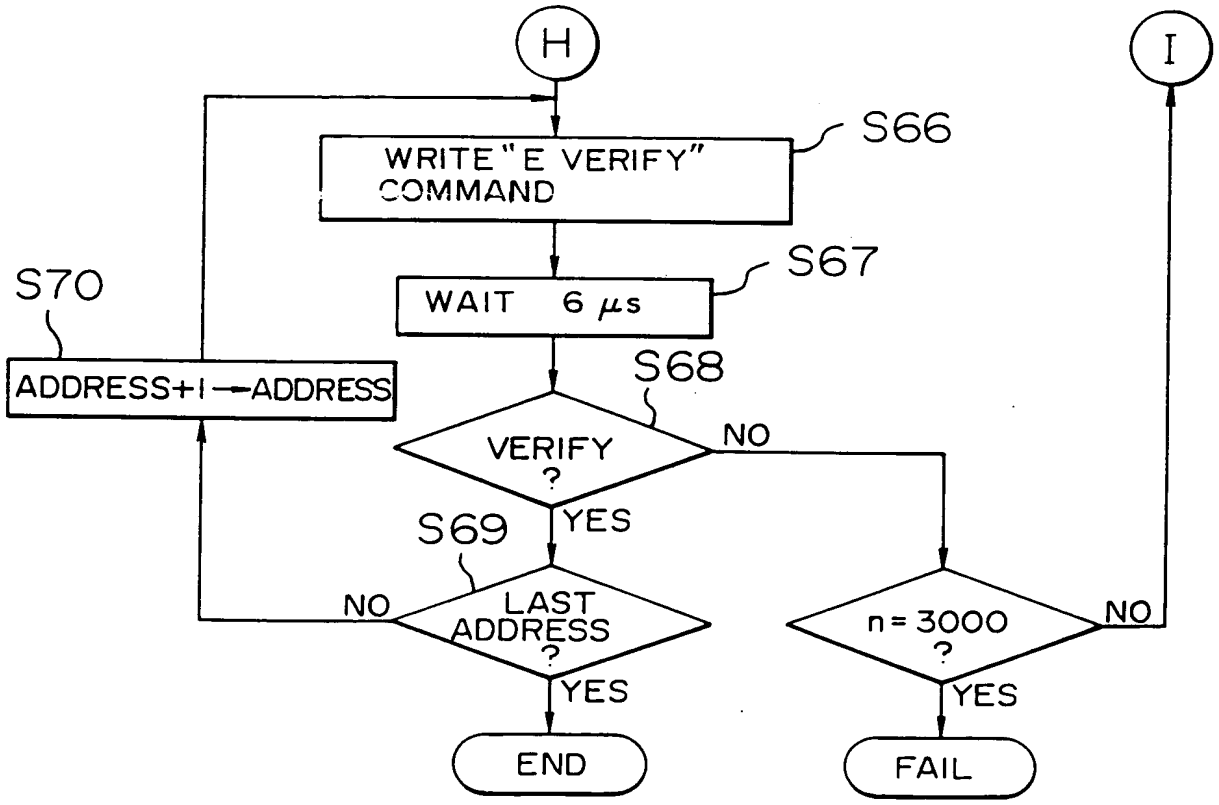


FIG. 22

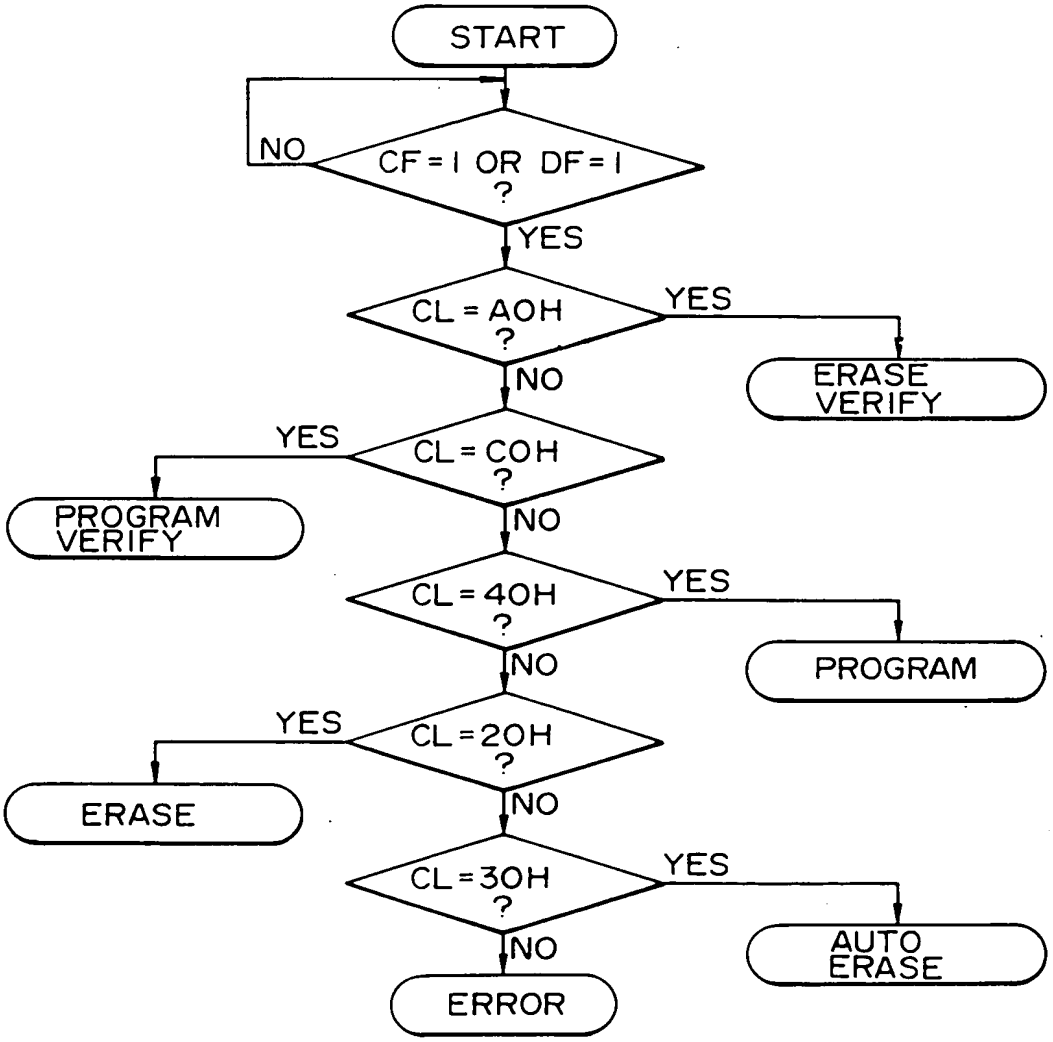


FIG. 23A

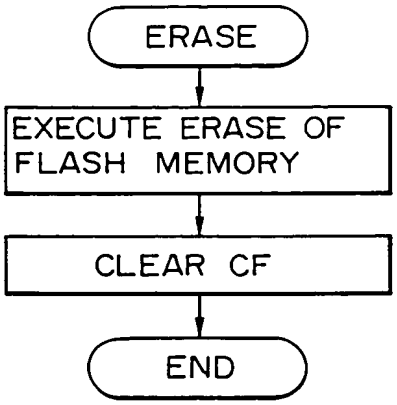


FIG. 23B

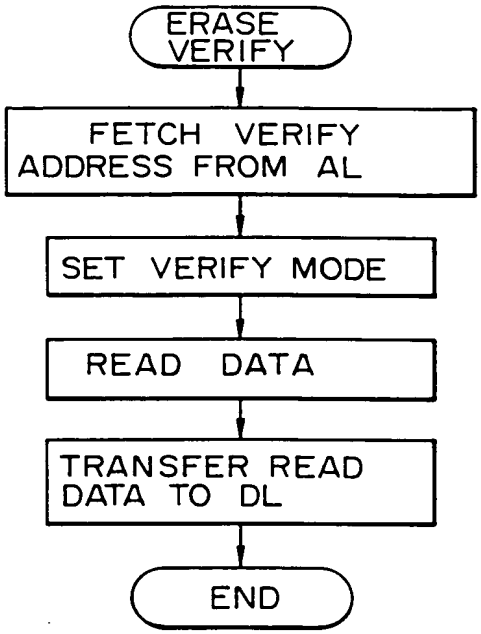
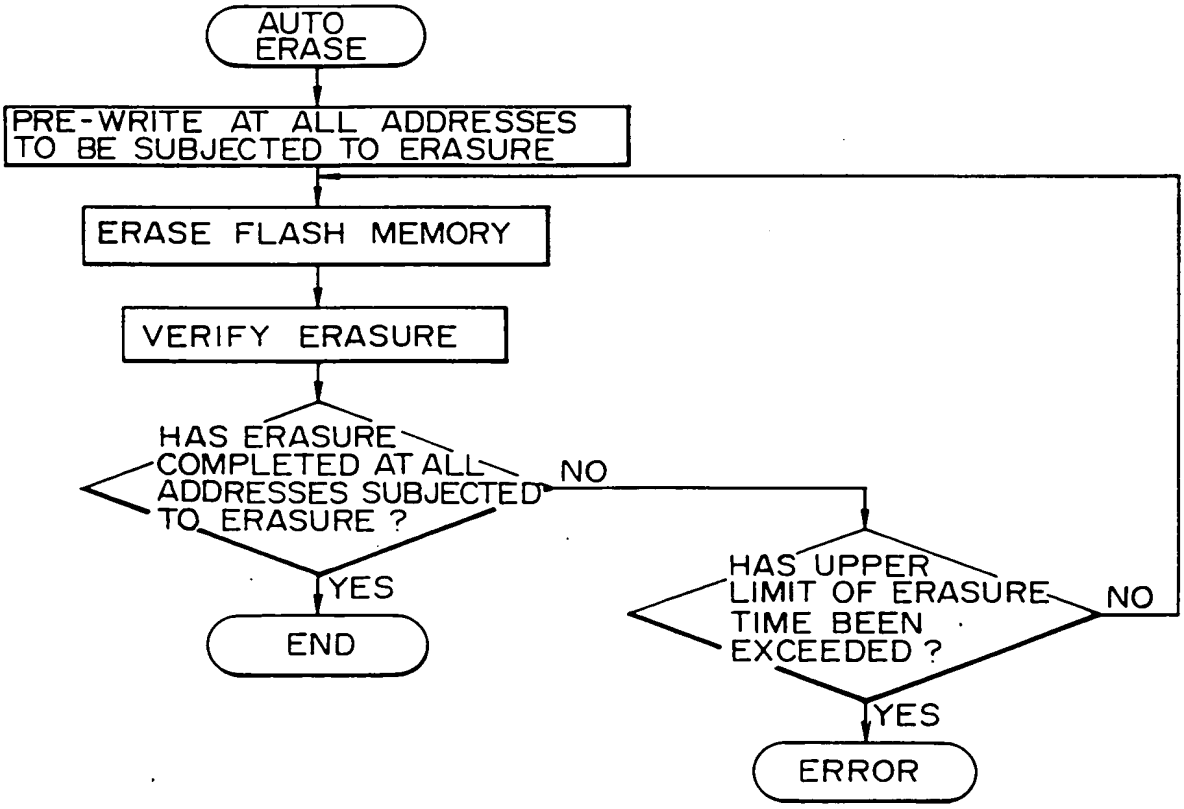
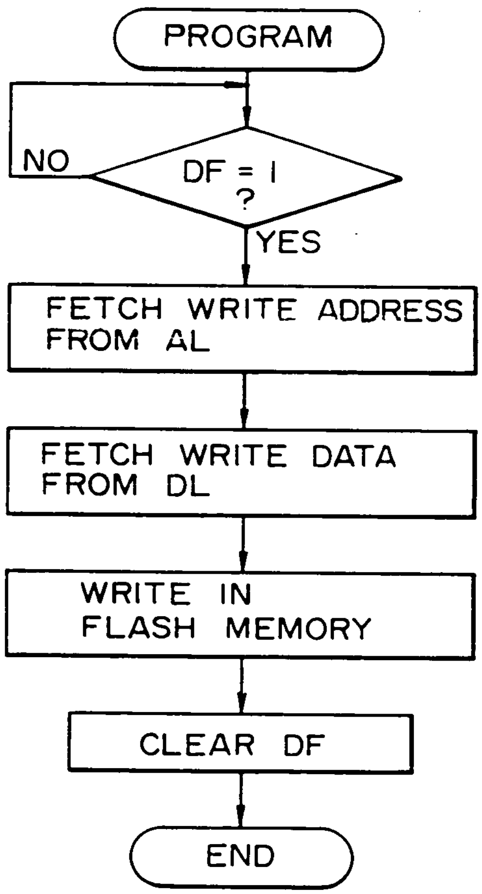


FIG. 24



F I G. 25 A



F I G. 25 B